OPCODES FAÍSKA

Código	Comando	Operação	Flags
00	MOV Rd, Rf	Rd <- Rf	-
01	SET Rd, Imm8	Rd <- ext (Imm8)	-
02	SET Rd, Imm32	Rd <- Imm32	-
03	LD Rd, label	Rd <- mem (label)	-
04	LD Rd, (Rf + Imm8)	Rd <- mem (Rf + ext (Imm8))	-
05	ST label, Rf	mem (label) <- Rf	-
06	ST (Rd + Imm8), Rf	mem (Rd + ext (Imm8)) <- Rf	-
07	LDB Rd, label	Rd <- mem8 (label)	-
08	LDB Rd, (Rf + Imm8)	Rd <- mem8 (Rf + ext (Imm8))	-
09	STB label, Rf	mem8 (label) <- Rf	-
0a	STB (Rd + Imm8), Rf	mem8 (Rd + ext (Imm8)) <- Rf	-
10	ADD Rd, Imm8	Rd <- Rd + ext (Imm8)	OCSZ
11	ADD Rd, Rf	Rd <- Rd + Rf	OCSZ
12	SUB Rd, Imm8	Rd <- Rd - ext (Imm8)	OCSZ
13	SUB Rd, Rf	Rd <- Rd - Rf	OCSZ
14	CMP Rd, Imm8	Rd - ext (Imm8)	OCSZ
15	CMP Rd, Rf	Rd - Rf	OCSZ
16	ADC Rd, Rf	Rd <- Rd + Rf + C	OCSZ
17	SBB Rd, Rf	Rd <- Rd - Rf - C	OCSZ
20	JMP label	IP <- Imm32	-
21	JMP Rf	IP <- Rf	-
22	JC Imm8	Se C = 1, IP <- IP + ext (Imm8)	-
23	JNC Imm8	Se C = 0, IP <- IP + ext (Imm8)	-
24	JZ Imm8	Se Z = 1, IP <- IP + ext (Imm8)	-
25	JNZ Imm8	Se Z = 0, IP <- IP + ext (Imm8)	-
26	JO Imm8	Se O = 1, IP <- IP + ext (Imm8)	-
27	JNO Imm8	Se O = 0, IP <- IP + ext (Imm8)	-
28	JS Imm8	Se S = 1, IP <- IP + ext (Imm8)	-
29	JNS Imm8	Se S = 0, IP <- IP + ext (Imm8)	-
2a	JL Imm8	Se S <> O, IP <- IP + ext (Imm8)	-
2b	JLE Imm8	Se S <> O ou Z = 1, IP <- IP + ext (Imm8)	-
2c	JG Imm8	Se S = F e Z = 0, IP <- IP + ext (Imm8)	-
2e	JGE Imm8	Se S = O, IP <- IP + ext (Imm8)	-
2f	JA Imm8	Se C = 0 e Z = 0, IP <- IP + ext (Imm8)	-
30	AND Rd, Rf	Rd <- Rd and Rf	OCSZ
31	OR Rd, Rf	Rd <- Rd or Rf	OCSZ
32	XOR Rd, Rf	Rd <- Rd xor Rf	OCSZ
33	TEST Rd, Rf	Rd and Rf	OCSZ
40	SHR Rd, Imm5	{C <- Rd0; RdI <- RdI+1; Rd31 <- 0} Imm5 vezes	OCSZ
41	SHR Rd, Rf	{C <- Rd0; RdI <- RdI+1; Rd31 <- 0} Rf vezes	OCSZ
42	SHL Rd, Imm5	{C <- Rd31; RdI+1 <- RdI; Rd0 <- 0} Imm5 vezes	OCSZ
43	SHL Rd, Rf	{C <- Rd31; RdI+1 <- RdI; Rd0 <- 0} Rf vezes	OCSZ
44	SAR Rd, Imm5	{C <- Rd0; RdI <- RdI+1} Imm5 vezes	OCSZ
45	SAR Rd, Rf	{C <- Rd0; RdI <- RdI+1} Rf vezes	OCSZ
48	ROR Rd, Imm5	{RdI <- RdI+1; C, Rd31 <- Rd0} Imm5 vezes	OCSZ
49	ROR Rd, Rf	{RdI <- RdI+1; C, Rd31 <- Rd0} Rf vezes	OCSZ
4a	ROL Rd, Imm5	{RdI+1 <- RdI; C, Rd0 <- Rd31} Imm5 vezes	OCSZ
4b	ROL Rd, Rf	{RdI+1 <- RdI; C, Rd0 <- Rd31} Rf vezes	OCSZ
4c	RCR Rd, Imm5	{RdI <- RdI+1; Rd31 <- C; C <- Rd0} Imm5 vezes	OCSZ
4d	RCR Rd, Rf	{RdI <- RdI+1; Rd31 <- C; C <- Rd0} Rf vezes	OCSZ
4e	RCL Rd, Imm5	{RdI+1 <- RdI; Rd0 <- C; C <- Rd31} Imm5 vezes	OCSZ
4f	RCL Rd, Rf	{RdI+1 <- RdI; Rd0 <- C; C <- Rd31} Rf vezes	OCSZ
50	PUSH Rf	SP <- SP - 4; mem (SP) <- Rf	-
51	POP Rd	Rd <- mem (SP); SP <- SP + 4	-
52	PUSHF	SP <- SP - 4; mem (SP) <- OITSZC	-
53	POPF	OITSZC <- mem (SP); SP <- SP + 4	OCSZ
54	CALL label	SP <- SP - 4; mem (SP) <- IP; IP <- label	-
55	CALL Rf	SP <- SP - 4; mem (SP) <- IP; IP <- Rf	-
56	RET	IP <- mem (SP); SP <- SP + 4	-
57	SYS imm8		-
58	IRET		OCSZ
60	IN Rd, Imm8		-
61	IN Rd, Rf		-
62	INB Rd, Imm8		-
63	INB Rd, Rf		-
64	OUT Imm8, Rf		-
65	OUT Rd, Rf		-
66	OUTB Imm8, Rf		-
67	OUTB Rd, Rf		-
70	CLC	C <- 0	C
71	STC	C <- 1	C
72	CLI	I <- 0	I
73	STI	I <- 1	I
74	HLT	Para a execução	-