

3.3 Memory map

See the datasheet corresponding to your device for a comprehensive diagram of the memory map. [Table 3](#) gives the boundary addresses of the peripherals available in all STM32F10xxx devices.

Table 3. Register boundary addresses

Boundary address	Peripheral	Bus	Register map
0xA000 0000 - 0xA000 0FFF	FSMC	AHB	Section 21.6.9 on page 539
0x5000 0000 - 0x5003 FFFF	USB OTG FS		Section 28.16.6 on page 884
0x4003 0000 - 0x4FFF FFFF	Reserved		
0x4002 8000 - 0x4002 9FFF	Ethernet		Section 29.8.5 on page 1040
0x4002 3400 - 0x4002 7FFF	Reserved		
0x4002 3000 - 0x4002 33FF	CRC		Section 4.4.4 on page 64
0x4002 2000 - 0x4002 23FF	Flash memory interface		
0x4002 1400 - 0x4002 1FFF	Reserved		
0x4002 1000 - 0x4002 13FF	Reset and clock control RCC		Section 7.3.11 on page 119
0x4002 0800 - 0x4002 0FFF	Reserved		
0x4002 0400 - 0x4002 07FF	DMA2		Section 13.4.7 on page 280
0x4002 0000 - 0x4002 03FF	DMA1		Section 13.4.7 on page 280
0x4001 8400 - 0x4001 7FFF	Reserved		
0x4001 8000 - 0x4001 83FF	SDIO		Section 26.9.16 on page 764

Table 3. Register boundary addresses (continued)

Boundary address	Peripheral	Bus	Register map
0x4001 5800 - 0x4001 7FFF	Reserved	APB2	
0x4001 5400 - 0x4001 57FF	TIM11 timer		Section 16.6.11 on page 449
0x4001 5000 - 0x4001 53FF	TIM10 timer		Section 16.6.11 on page 449
0x4001 4C00 - 0x4001 4FFF	TIM9 timer		Section 16.5.13 on page 440
0x4001 4000 - 0x4001 4BFF	Reserved		
0x4001 3C00 - 0x4001 3FFF	ADC3		Section 11.12.15 on page 243
0x4001 3800 - 0x4001 3BFF	USART1		Section 27.6.8 on page 799
0x4001 3400 - 0x4001 37FF	TIM8 timer		Section 14.4.21 on page 348
0x4001 3000 - 0x4001 33FF	SPI1		Section 25.5 on page 714
0x4001 2C00 - 0x4001 2FFF	TIM1 timer		Section 14.4.21 on page 348
0x4001 2800 - 0x4001 2BFF	ADC2		Section 11.12.15 on page 243
0x4001 2400 - 0x4001 27FF	ADC1		Section 11.12.15 on page 243
0x4001 2000 - 0x4001 23FF	GPIO Port G		Section 9.5 on page 188
0x4001 1C00 - 0x4001 1FFF	GPIO Port F		Section 9.5 on page 188
0x4001 1800 - 0x4001 1BFF	GPIO Port E		Section 9.5 on page 188
0x4001 1400 - 0x4001 17FF	GPIO Port D		Section 9.5 on page 188
0x4001 1000 - 0x4001 13FF	GPIO Port C		Section 9.5 on page 188
0x4001 0C00 - 0x4001 0FFF	GPIO Port B		Section 9.5 on page 188
0x4001 0800 - 0x4001 0BFF	GPIO Port A		Section 9.5 on page 188
0x4001 0400 - 0x4001 07FF	EXTI		Section 10.3.7 on page 205
0x4001 0000 - 0x4001 03FF	AFIO	Section 9.5 on page 188	