

Seminários

Abaixo encontram-se relacionados sugestões de temas para seminários. No final inclui uma lista das principais fontes onde podem ser encontrados artigos sobre os temas. Alguns temas acompanham uma sugestão de referências para servir como ponto de partida e outros estão também presentes nos livros sugeridos para a disciplina, principalmente no Muchnick.

Os temas estão sendo reservados por ordem de chegada dos pedidos. Os já ocupados estão anotados com os respectivos nomes dos alunos responsáveis pelo seminário.

• TEMAS

1. Otimização Dinâmica (TATIANE)
 - a. <http://portal.acm.org/citation.cfm?id=1048922.1048976&coll=ACM&dl=ACM&CFID=57820351&CFTOKEN=50071054>
 - b. Mathias Payer Florian T. Schneider and Thomas R. Gross. Online optimizations driven by hardware performance monitoring. ACM SIGPLAN Notices , Proceedings of the 2007 ACM SIGPLAN conference on Programming language design and implementation PLDI 07, June 2007.
2. Tradução Binária (FERNANDA)
 - a. <http://portal.acm.org/citation.cfm?id=351403.351414&coll=Portal&dl=ACM&CFID=62113957&CFTOKEN=59012550>
 - b. Shu Xu Bo Huang Jianhui Li, Qi Zhang. Optimizing dynamic binary translation for simd instructions. Proceedings of the International Symposium on Code Generation and Optimization CGO 06, March 2006.
 - c. D. Ung and C. Cifuentes. Optimising hot paths in a dynamic binary translator. In Workshop on Binary Translation, October 2000.
3. Otimizações para consumo de energia (FELIPE)

Bypass Aware Instruction Scheduling for Register File Power Reduction
Sanghyun Park (*Seoul National University, South Korea*)
Aviral Shrivastava (*University of California, Irvine, USA*)
Nikil Dutt (*University of California, Irvine, USA*)
Alex Nicolau (*University of California, Irvine, USA*)
Yunheung Paek (*Seoul National University, South Korea*)
Eugene Earlie (*Intel Corporation, USA*)

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Energy Efficient Cross-path Scheduling for Clustered VLIW Processors
Rahul Nagpal (*Dept. of Computer Science and Automation, Indian Institute of Science, Bangalore, India*)

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4. Thread-level Speculation (TLS) (TICIANA)
 - a. Speculative Synchronization: Applying Thread-Level Speculation to Explicitly Parallel Applications. Jose F. Martinezy and Josep Torrellas.
 - b. Speculative Lock Elision: Enabling Highly Concurrent Multithreaded Execution. Ravi Rajwar and James R. Goodman.
5. Otimizações para tempo-real (WCET)

Faster WCET Flow Analysis by Program Slicing

Christer Sandberg (*Dept. of Computer Science and Electronics, Mälardalen University, Västerås, Sweden*)

Andreas Ermedahl (*Dept. of Computer Science and Electronics, Mälardalen University, Västerås, Sweden*)

Jan Gustafsson (*Dept. of Computer Science and Electronics, Mälardalen University, Västerås, Sweden*)

Bjö Lisper (*Dept. of Computer Science and Electronics, Mälardalen University, Västerås, Sweden*)

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<http://portal.acm.org/citation.cfm?id=1113841.1113842>
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6. Cache related optimizations
 - a. <http://portal.acm.org/citation.cfm?id=375977.375978>
&coll=ACM&dl=ACM&CFID=57820351&CFTOKEN=50071054
 - b. (Cap 20 Muchnick)
 - c.
7. Interprocedural Analysis and Optimizations (RICARDO)
 - a. (Cap 19 Muchnick)
8. Trace trees (JOÃO)
 - a. Making the Compilation “Pipeline” Explicit: Dynamic Compilation Using Trace Tree Serialization. Andreas Gal, Michael Bebenita, Mason Chang, and Michael Franz
9. Speculative Decoupled Software Pipelining (Ottoni/August)
 - a. Speculative Decoupled Software Pipelining. Neil Vachharajani Ram Rangan† Easwaran Raman Matthew J. Bridges Guilherme Ottoni David I. August
10. Auto-paralelização (CRISTIANNO)
11. Vetorização (BRUNO)
 - a. (Cap 12 Wolfe)
12. Link time optimization
 - a. <http://llvm.cs.uiuc.edu/docs/LinkTimeOptimization.html>
 - b. <http://portal.acm.org/citation.cfm?id=997163.997194>
&coll=ACM&dl=ACM&CFID=57820351&CFTOKEN=50071054

- **Conferências**

- DATE
- DAC
- CGO
- PLDI
- LCTES

- **Revistas**
- TACO
- TOPLAS
- TODAES
- Transactions ACM e IEEE de Embedded systems, Linguagens de Programação, etc